
Career Highlights

- At HP, led a team that shipped a HP-branded IP connected TV to retailer Best Buy, an industry first.
- At C-Cube Microsystems (now LSI Logic), led a team that took six DVB set-top box customers to volume production generating over \$40M in chip revenues.
- At Digital 5, drove two Tier-1 customer wins in nascent connected home market; wins include digital media receiver and media server for content streaming to-and-thru the digital home.
- At Intel, built a twenty-person apps team and delivered production-quality networking apps to Cisco, Nokia, & Alcatel.
- At Microtec Research, product-managed the embedded tools products from zero to \$4M revenue.
- My patents; 6,981,274, 7,110,542, 6,825,874. Active in patent harvesting from projects.

Jan 05 –
Feb 07

Software Manager, Digital TV Solutions, HP

- Led a team of software & systems architects that shipped the first internet connected TV with direct-connect to internet photo, music, and video services. Product sold by retailer Best Buy.
- Led a team of software architects that prototyped an internet & cable connected hi-def digital media hub.

Aug 03 –
Dec 04

Director, Technical Marketing, Digital5 Inc.(acquired by Mediabolic)

- Technical lead and sales lead on two Tier 1 customer wins. Generated media server and media adapter requirements for the streaming-to-and-through-the-home market.
- Educated OEMs through white papers and technical briefs on streaming, security, and quality-of-service for connected devices.
- Worked with premium movie and music services to distill requirements for home streaming.

Oct 02 –
Aug 03

Director, Technical Marketing, TriMedia Technologies (now Philips)

- Led a team that delivered three detailed SoC-based technical proposals; a media-processor based DVD-recordable engine, a H.264 High Definition DVD engine, and a 40 Mega Pixels/sec image-processing engine for printers.
- Proposed new instructions for the Trimedia architecture to meet performance goals.
- Fixed software defects in record time in Linux driver for IPSec & SSL security processor so the OEM (Abit) could go to production. Provided pre-sales support to Tier-1 networking OEMs to design-in security silicon.

Jan. 98–
Sept. 02

Application Engineering Manager; Intel, San Jose

- Contributed to the architecture of the programmable transport demultiplexor for satellite set-top boxes. Wrote the DVB and DSS microcode and driver. Tested it on FPGA. Filed **three** patents.
- Delivered network processor-based Wireless Router, ATM DSLAM, Intrusion Detection apps, and soft CAMs to Tier-1 OEMs. Managed **ten** software developers engaged in Network Processor-based application development for Tier-1 OEMs.
- Led a team that delivered IPv6 router, header compression, traffic management/shaping, classifier, and NAT engine.

Oct 95 –
Dec 97

Technical Marketing Manager, Set Top & DVD, C-Cube Microsystems (now LSI Logic)

- Managed the MPEG-2 set-top box Technical Marketing team, took **six** set-top box customers into volume production; revenue generated, \$40M.
 - Specified hardware & software for C-Cube's set-top box reference design; single-handedly wrote the drivers for the set-top chip-set (audio/video & transport).
 - Won excellence award for providing Nokia with proactive engineering support.
 - Hands-on technical management in addition to maintaining software MPEG-2 decoder API and trouble-shooting customer issues
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Aug 90 –
Apr 95

Product Manager, Microtec Research (now Mentor), Inc., Santa Clara, CA

- Grew the C/C++ embedded RISC tools sales from zero to \$4M in three years.
- Managed the first C++ integrated development environment for embedded system developers, a major new product and direction for the company. Sold 150 seats in the first 12 months.
- Conceived and managed the introduction of the first C++ compiler and debugger for Intel's i960 32-bit embedded micro-controller.
- Published nine technical articles and presented at three technical conferences.
- Wrote three product C++ demonstration packages for self-paced guide.
- Defined worldwide beta-testing process and free product evaluation process.
- Designed and executed a direct-mail piece with a record response of 5%.

Aug 88 –
Aug 90

Software Development Engineer, Hewlett Packard Corp., Santa Clara, CA

- Designed and coded a 2000-line C program to create the user interface for a real-time multitasking instrument containing a Motorola 68000 processor.
- Implemented in assembly language, highly optimized graphics algorithms for the same screen-based real-time instrument.
- Used the *TeamWork* Structured Design and Structured Analysis CASE tool to communicate requirements and designs in a large software team.

Aug. 87–
Aug. 88

CAD Software Engineer, PHILIPS Applications Specific Products Div., Sunnyvale, CA

- Added new features and maintained existing features in a 10,000-line CAD program that converted gate-level schematic diagrams into boolean equations.
- Designed, implemented, and documented in three weeks, a PC-hosted graphical waveform analysis tool using Microsoft C.
- Implemented a graphical fuse-table editor for programmable logic devices in one week.

Jul. 84 –
Aug. 87

Image Processing Manager & Engineer, View Engineering, Simi Valley, CA

- Managed a cross-functional team of software, hardware, mechanical, and optical engineers to design and implement two vision-systems; one for food inspection, another for semiconductor wafer inspection. The food inspection vision system was delivered ahead of schedule leading to \$40,000 early delivery reward for the company.
- Designed image-processing algorithms for detecting corners in images installed in vision systems that compute the location and orientation of industrial parts.
- Implemented image processing primitives such as blob counting and area/centroid calculations in the AMD 29116 micro-code language. Exploited a highly parallel image processing architecture to meet aggressive system performance requirements.

Education

- **MS Computer Science**, 1984, Wayne State University, Detroit, Michigan, GPA 4.0/4.0.
 - **B. Tech.** in Computer Science and Engineering, 1982, **Indian Institute of Technology**, Kharagpur, India.
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